

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

Claim 1 (Currently amended) A panel, comprising:

a first side, including:

at least one first connector configured to connect to electrical equipment and receive a plurality of signals from the electrical equipment, the at least one first connector including an integrated connector having a number of contact points therein, and

a plurality of second connectors respectively configured to connect to a plurality of network devices and deliver the plurality of signals to the plurality of network devices; and

a second side opposite from the first side, including:

a plurality of third connectors respectively coupled to at least one of the at least one first connector and the plurality of second connectors and configured to provide temporary connection to at least one of the electrical equipment and the plurality of network devices.

Claim 2 (Original) The panel of claim 1, wherein the second connectors include wire wrap pins.

Claim 3 (Canceled)

Claim 4 (Original) The panel of claim 1, wherein the at least one first connector is electrically connected to the plurality of second connectors to deliver the plurality of signals from the electrical equipment to the plurality of network devices.

Claim 5 (Original) The panel of claim 1, wherein the plurality of third connectors includes:

a plurality of fourth connectors electrically connected to the at least one first connector and configured to provide temporary connections from the electrical equipment.

Claim 6 (Original) The panel of claim 5, wherein the plurality of third connectors further includes:

a plurality of fifth connectors electrically connected to the plurality of second connectors and configured to provide temporary connections to the plurality of network devices from the plurality of fourth connectors.

Claim 7 (Original) The panel of claim 5, wherein the plurality of third connectors further includes:

a plurality of sixth connectors electrically connected to the plurality of second connectors via a respective plurality of resistors and configured to provide high impedance connections to the plurality of network devices.

Claim 8 (Original) The panel of claim 1, wherein the at least one first connector and the plurality of second connectors each includes a primary group of connectors and a secondary group of connectors for connecting primary and secondary groups of signals between the electrical equipment and the plurality of network devices.

Claim 9 (Currently amended) A timing output panel, comprising:

a rear portion, including:

a plurality of network connectors respectively configured to connect to a plurality of network elements, and

at least one timing connector connected to the plurality of network connectors and configured to connect to synchronization electronics, the at least one timing connector including a unitary connector having multiple pins corresponding to a number of signals from the synchronization electronics; and

a front portion, including:

a plurality of equipment jacks corresponding to and electrically connected to the plurality network connectors, the equipment jacks facilitating temporary connection of cables for testing or patching signals to the network elements, and

a plurality of timing jacks corresponding to and electrically connected to the at least one timing connector, the timing jacks facilitating temporary connection of cables for testing the synchronization electronics or patching to the equipment jacks.

Claim 10 (Original) The timing output panel of claim 9, wherein the network connectors include wire wrap pins.

Claim 11 (Canceled)

Claim 12 (Original) The timing output panel of claim 9, wherein the front portion further includes:

a plurality of monitor jacks electrically connected to the plurality of equipment jacks by a respective plurality of resistors.

Claim 13 (Original) The timing output panel of claim 9, wherein the network connectors include:

a primary group of network connectors, and  
a secondary group of network connectors that are spatially separated from the primary group of network connectors.

Claim 14 (Original) The timing output panel of claim 9, wherein the at least one timing connector include:

a primary timing connector, and  
a secondary timing connector that is spatially separated from the primary timing connector.

Claim 15 (Original) The timing output panel of claim 9, wherein the equipment jacks include:

a primary group of equipment jacks, and  
a secondary group of equipment jacks that are spatially separated from the primary group of equipment jacks.

Claim 16 (Original) The timing output panel of claim 12, wherein the equipment jacks, the timing jacks, and the monitor jacks include an identical type of connector.

Claim 17 (Currently amended) A panel, comprising:

a rear portion, including:

means for connecting to a plurality of network elements, and

means for connecting to synchronization electronics; and  
a front portion, including:  
means for testing at least one of the plurality of network elements and the  
synchronization electronics, wherein  
the means for connecting to synchronization electronics includes at least one  
connector including an integrated connector having a number of contact points therein.

Claim 18 (Original) The panel of claim 17, wherein the front portion further includes:  
means for monitoring signals associated with the synchronization electronics.

Claim 19 (Original) The panel of claim 17, wherein the means for testing includes:  
means for removably accepting a patch cable.

Claim 20 (Original) The panel of claim 17, wherein the means for connecting to the  
plurality of network elements includes:  
means for perpetually receiving and holding one or more connection wires.

Claim 21 (Original) A system, comprising:  
a plurality of network elements, each network element respectively receiving an input  
timing signal;  
a timing signal generator configured to generate the synchronization signals for the  
plurality of network elements; and  
a timing output panel connected between the plurality of network elements and the timing  
signal generator, the timing output panel including:

at least one first connector configured to receive the output signals from the timing signal generator,  
a plurality of second connectors configured to send the output signals to the plurality of network elements, and  
a plurality of third connectors spaced apart from the first and second connectors and configured to facilitate testing of the output signals and patching of the output signals.

Claim 22 (New) A timing output panel comprising:

a first side, including:

a plurality of first connectors configured to be connected to respective reference timing inputs of network elements, the plurality of first connectors including a plurality of primary first connectors and a plurality of secondary first connectors, the first connectors including standard Bantam type intrusive DSX-1 jacks for temporary connections;

a plurality of second connectors, the second connectors being configured to connect to respective outputs of a timing signal generator, each of the second connectors including standard Bantam type intrusive DSX-1 jacks for temporary connections; and

a plurality of monitor connectors, the monitor connectors including a plurality of primary monitor connectors and a plurality of secondary monitor connectors configured to be connected to respective outputs of the timing signal generator via resistors, the monitor connectors including standard Bantam type intrusive DSX-1 jacks for temporary connections; and

a second side including:

a plurality of tip and ring connectors configured to be connected to one of the reference timing inputs of the network elements using 1-pair shielded cables and respective contact points of the first connectors within the timing output panel, the tip and ring connectors including feed-through wire wrap pins; and

a primary and a secondary timing signal generator connector, each of the timing signal generator connectors including a 50-pin amphenol-type connector configured to be connected to the outputs of the timing signal generator.